

Fig. 1

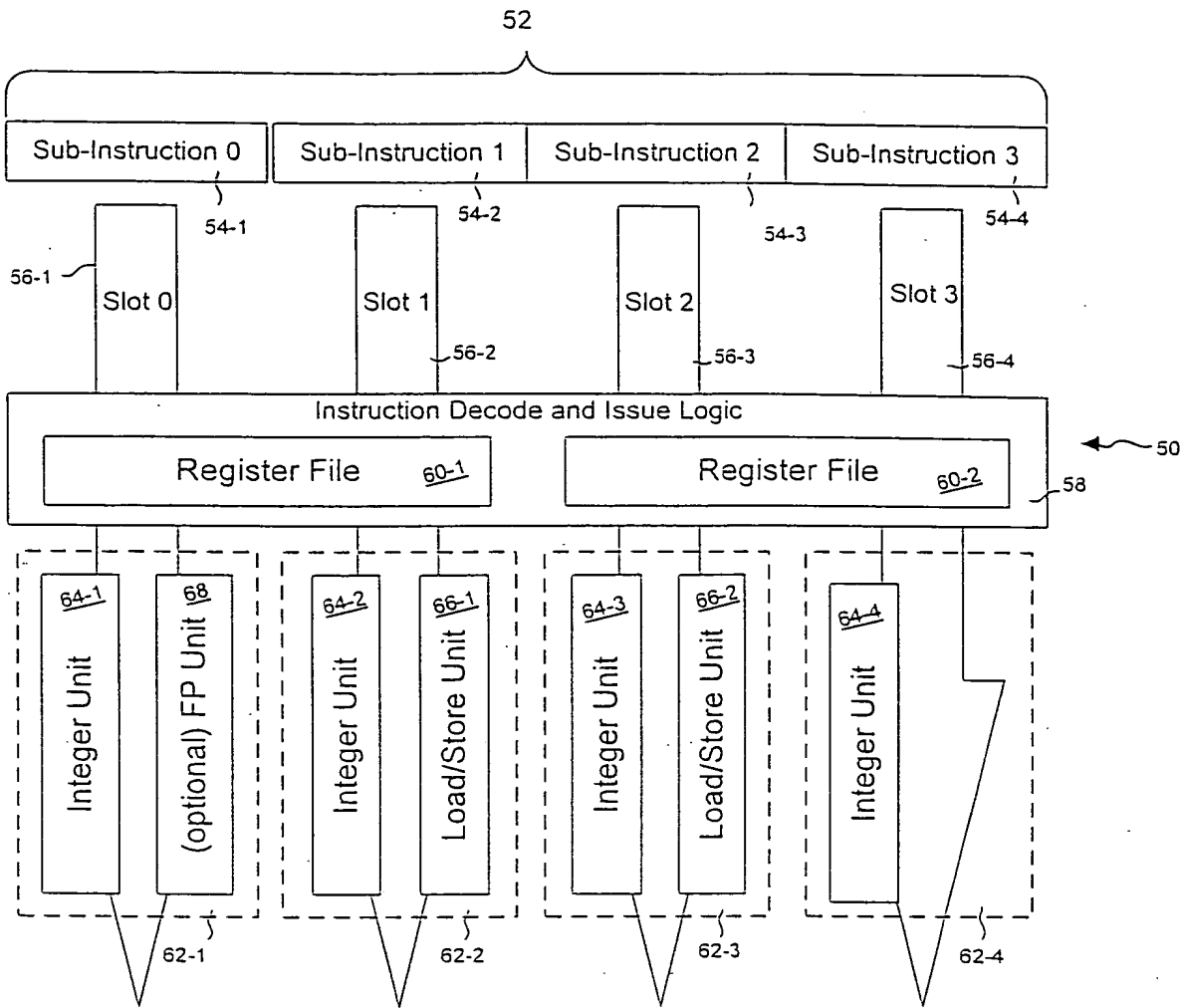


Fig. 2

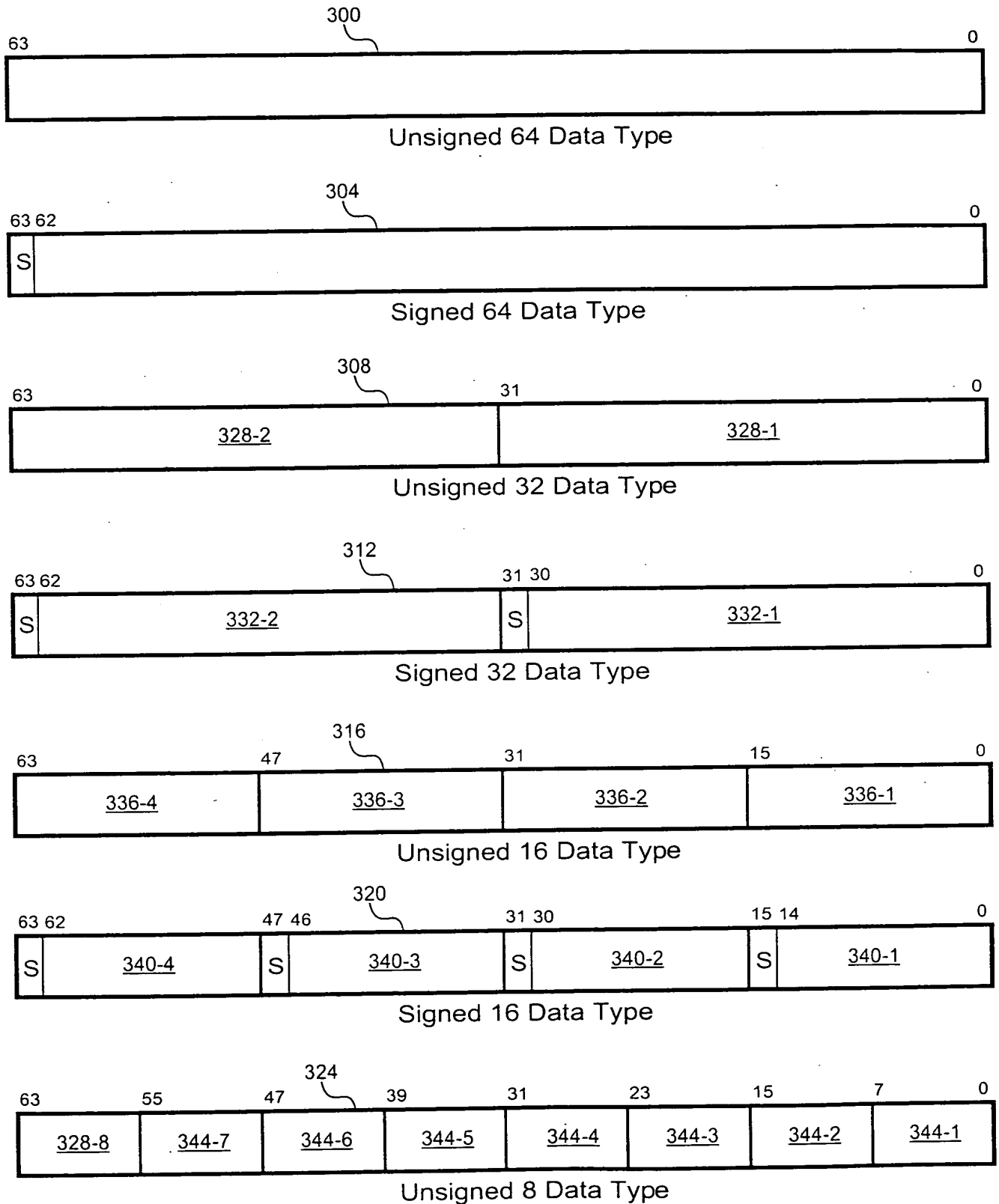
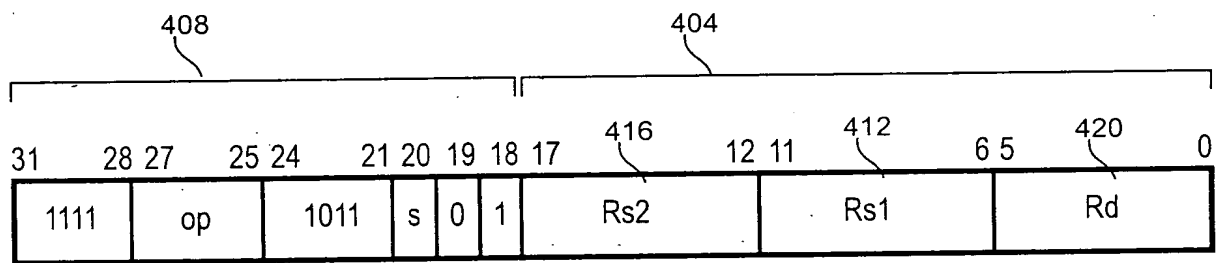


Fig. 3



TRANS Register Sub-Instruction: 16 bit Elements

Fig. 4

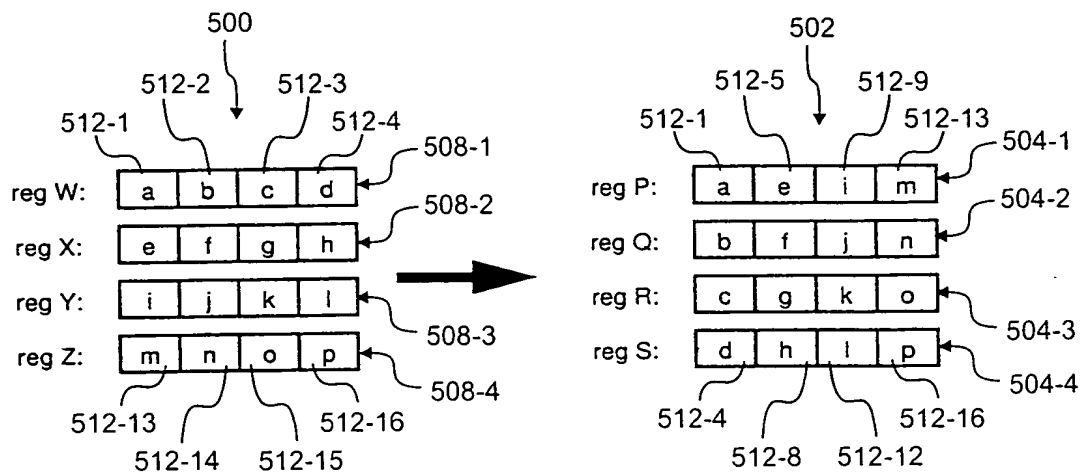


Fig. 5

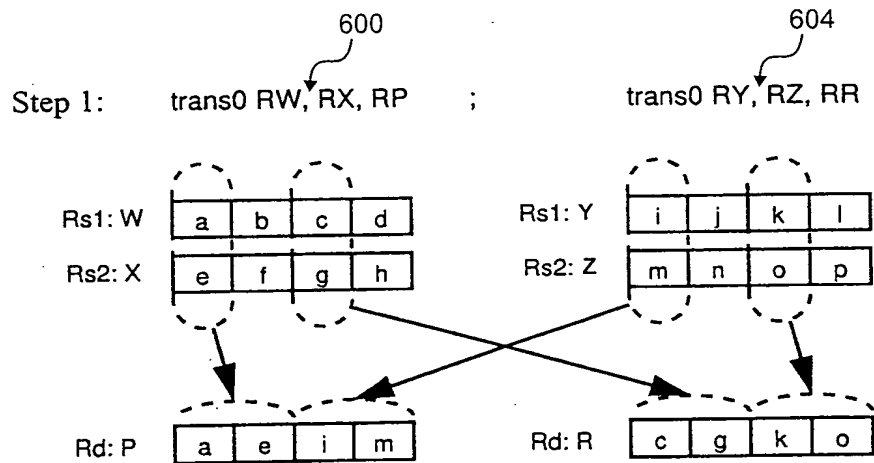


Fig. 6A

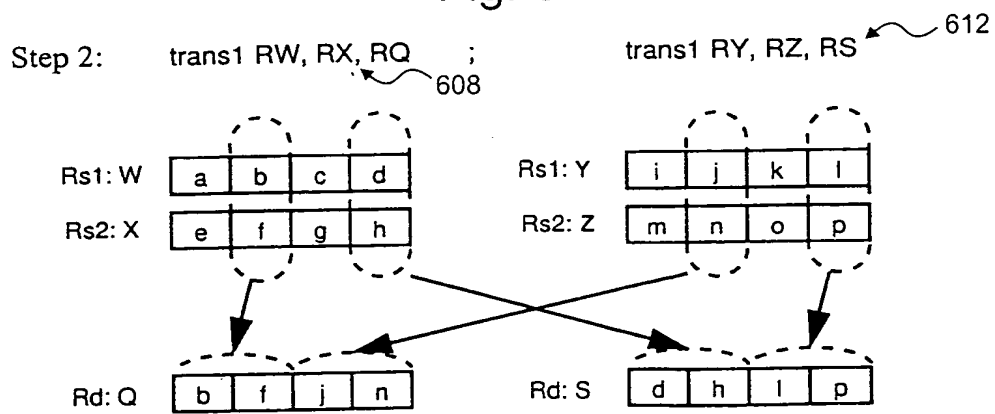


Fig. 6B

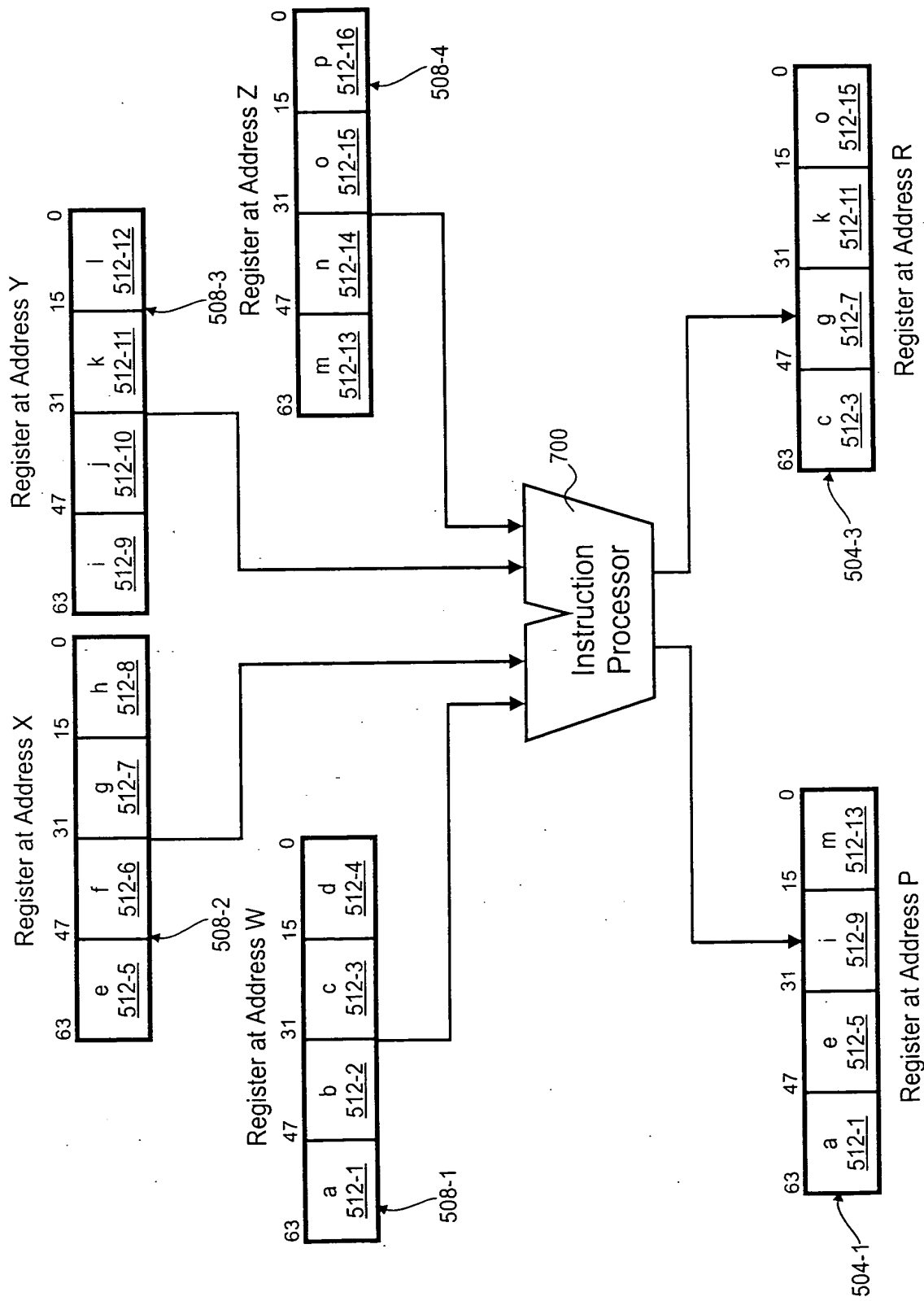


Fig. 7

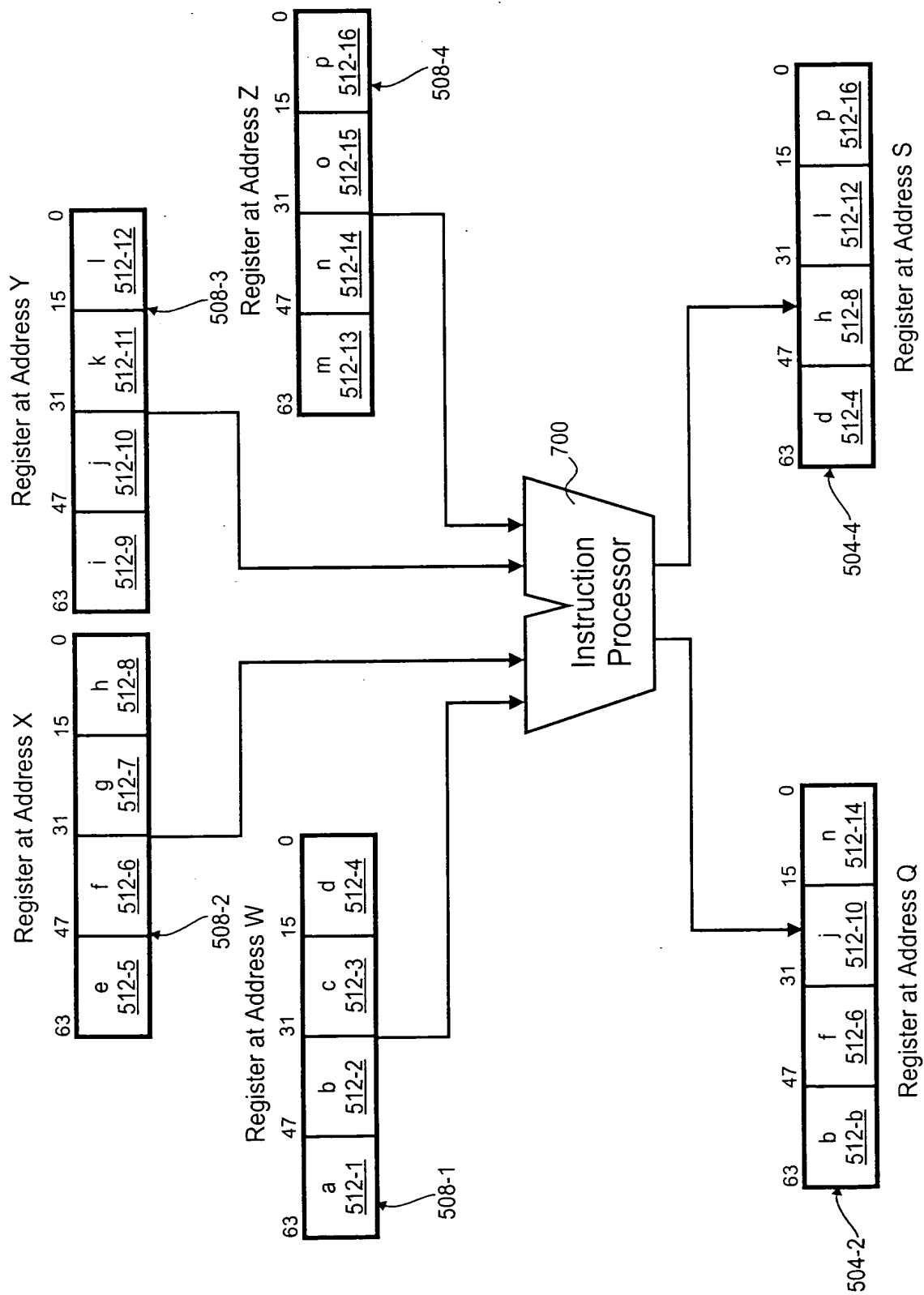


Fig. 8

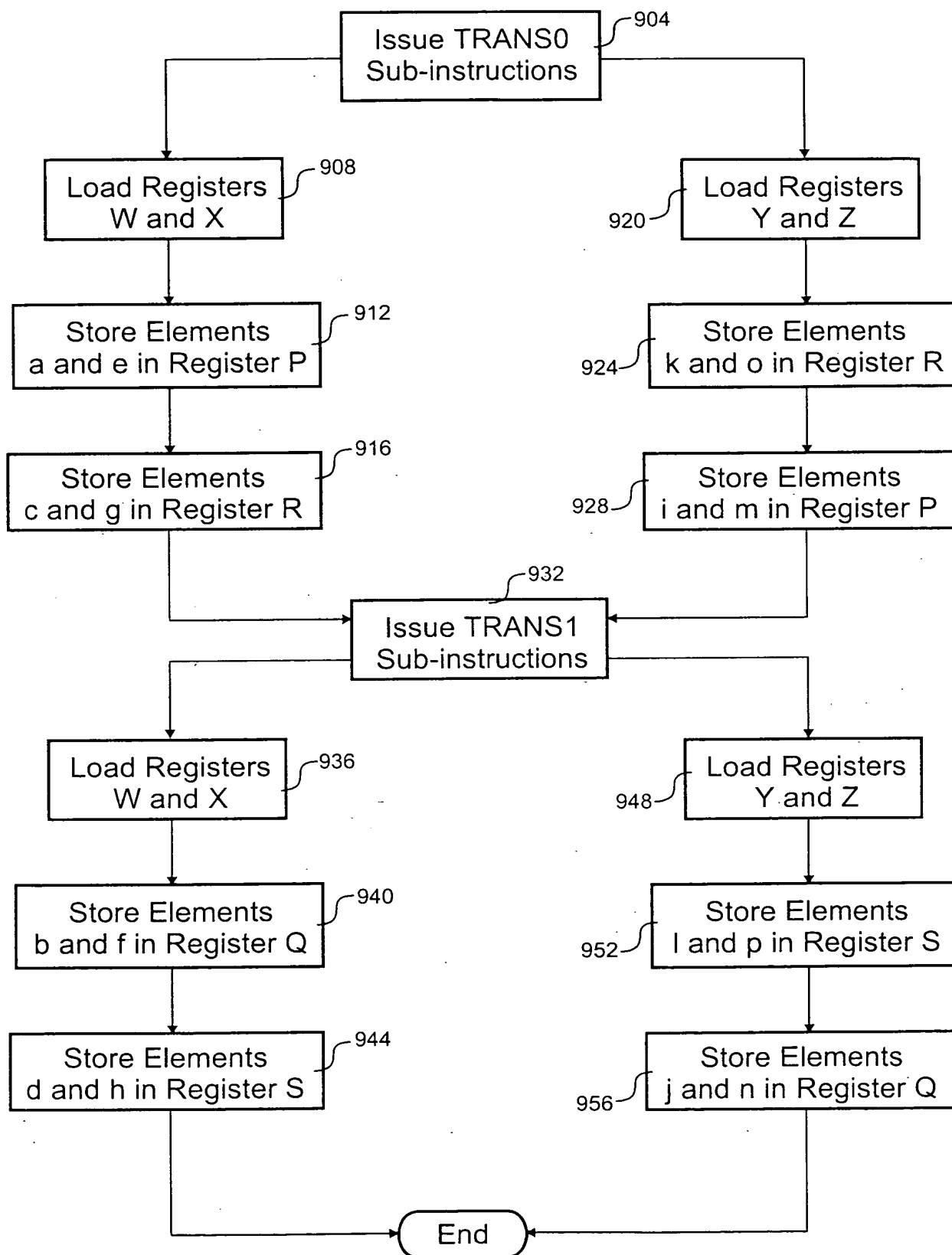


Fig. 9



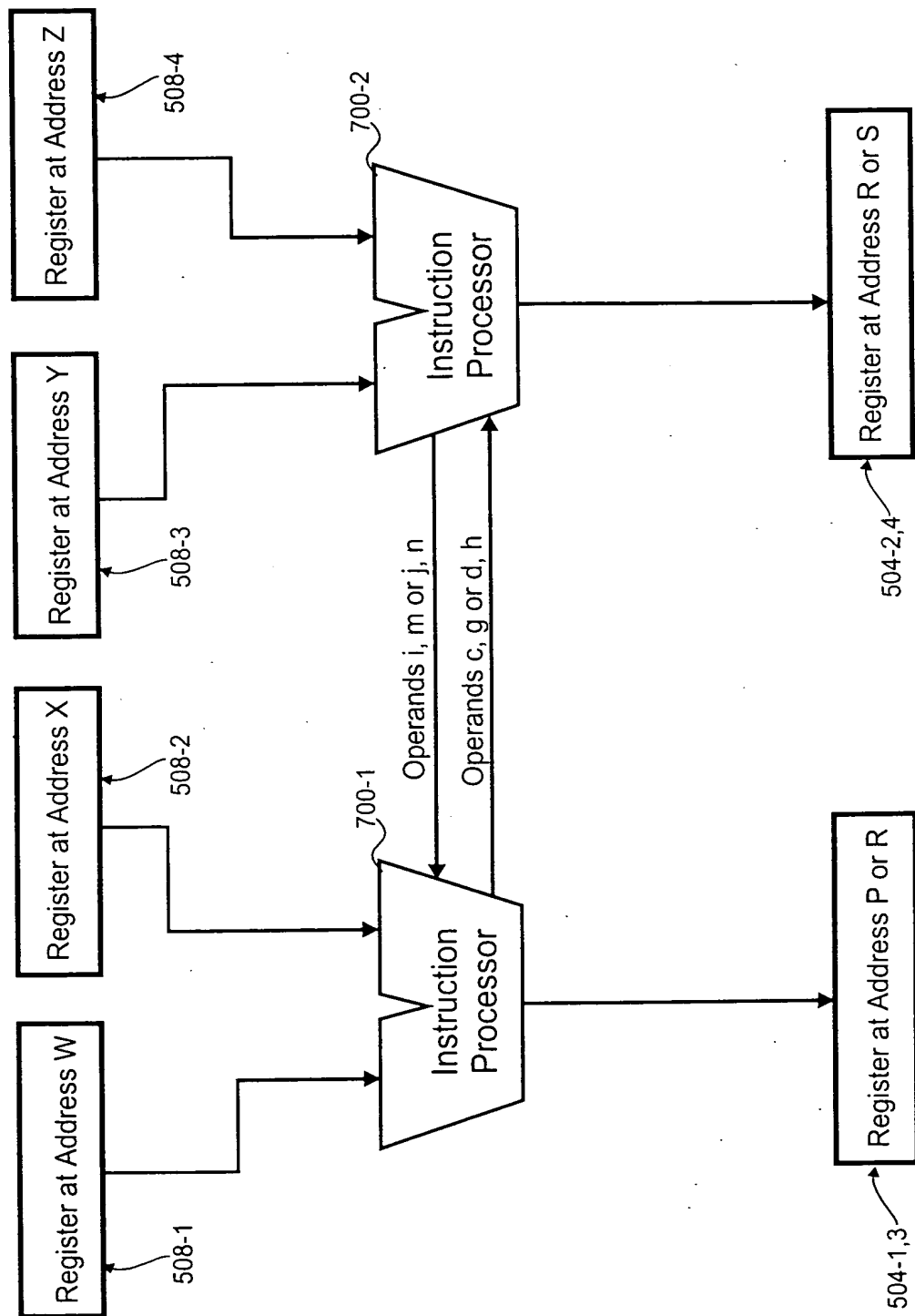


Fig. 10